



2nd INTERNATIONAL CONFERENCE ON ADVANCEMENTS IN POWER, COMMUNICATION AND INTELLIGENT SYSTEMS (APCI 2025)

**Government College of Engineering Kannur
Kannur, Kerala, India**

27th – 28th June 2025

Hybrid Mode *

VENUE:

Government College of Engineering Kannur

Sponsors:

Government College of Engineering Kannur (DTE Kerala)

IEEE Kerala Chapter (Technical Co-sponsor)

 <https://www.gcek.ac.in/APCI2025>

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*** Accepted papers with presenting author from outside Kerala and international authors will be allowed to present ONLINE MODE**

About APCI

2nd International conference on Advancements in Power, Communication, and Intelligent systems (APCI 2025) will be held in hybrid mode during 27th, 28th June 2025, at Government College of Engineering Kannur, Kerala, India.

This conference is a step to encourage researchers and professionals for sharing the developments in power, communication, signal processing, electronic systems, artificial intelligence and related areas. The participants from academia, industry and research organisations come closer through presentation sessions, keynote lectures and exhibitions. The conference offers chance for researchers to share their novel thoughts related to current and future technologies in power, communication and intelligent systems and hence provide an excellent opportunity for interaction, discussion and dissemination of the latest research and developments.

We invite all leading researchers, engineers and scientists in the domain of interest from around the world. We warmly welcome authors to submit research papers to International Conference APCI 2025, and share their valuable experiences and thoughts with the scientific community.

International Conference on Advancements in Power, Communication, and Intelligent Systems started in the year 2024. First edition of the conference was conducted in hybrid mode on 21st and 22nd June 2024 at Government College of Engineering Kannur, Kerala, India. The conference was organized by Government College of Engineering Kannur with the support of APJ Abdul Kalam Technological University and Department of Technical Education, Kerala. The conference was technically co-sponsored by IEEE Kerala Section. Conference had nine different tracks in which research papers were presented. Tracks were 1. Power and Renewable Energy Systems, 2. Electric Drives and Power Converters, 3. Sensors, Instrumentation and Automation Systems, 4. VLSI, 5. Embedded Systems and IoT, 6. Signal Processing, 7. Communication Systems, 8. Artificial Intelligence and Machine Learning, 9. Cloud Computing.

About GCE Kannur

Government College of Engineering, Kannur is one of the premier institutes among the 9 Government Engineering Colleges in Kerala and was established in 1986. The college is functioning in a sprawling 68-acre scenic campus, having sound and self-sufficient infrastructure, at Mangattuparamba, near the National Highway, 15 km from the headquarters of Kannur district. The college is affiliated to Kerala Technological University. It offers 5 B. Tech degree programs in Civil, Mechanical, Electrical and Electronics, Electronics and Communication and Computer Science and Engineering branches. The College also offers 8 M. Tech programs in Civil Engineering (Computer Aided Structural Engineering, Geotechnical and Geo environmental Energy), Mechanical Engineering (Advanced Manufacturing and Mechanical Systems Design, Energy Engineering), Electrical and Electronics Engineering (Power Electronics and Drives, Power Systems), Electronics and Communication Engineering (Signal Processing and Embedded Systems) and Computer Science and Engineering (Artificial Intelligence and Data Science). Doctoral programs are offered under Kerala Technological University. The institution is recognized by Kerala Technological University as a 'Centre of Excellence in Systems, Energy and Environment'.

Scope of the Conference

Track 1: Power and Renewable Energy Systems:

Smart grid, Power quality, Electronic systems for energy applications, Energy conservation and management.

Track 2: Electric Drives and Power Converters:

Electric Drives, Electric Vehicles, Control and Topologies of Power Converters.

Track 3: Sensors, Control, and Automation Systems:

Power and biomedical applications, Control systems, Instrumentation systems.

Track 4: VLSI:

Analog design, Digital design, CAD based design, Devices and Circuits, Semiconductor devices and sensors, ASIC and FPGA design, Nano-electronics, Low power design, Testing and verification, Quantum Computing.

Track 5: Embedded Systems and IoT:

Processor design, Microprocessor/Microcontroller based design, System design, IoT, Sensors and actuators based systems, Wireless sensor network, Robotics, Embedded protocols.

Track 6: Signal Processing:

Image and video processing, Audio/Speech processing, Biomedical signal processing, Pattern recognition, Signal analysis and modelling, Signal compression and coding, Statistical signal processing, AI/ML in signal processing.

Track 7: Communication Systems:

Wireless/Mobile communication, Optical communication, Satellite communication, Network protocols, Information theory and coding, Cognitive radio, Radar and Antennas.

Track 8: Artificial Intelligence and Machine Learning:

Virtual and Augmented reality, Generative AI and large language model, Natural language processing and machine translation, Social network behaviours, Modelling and analysis, Big data analytics.

Track 9: Cloud Computing:

Cloud computing, High performance computing.

Track 10: Cyber Security and Cryptography:

Cryptography, Cyber security.

* Note: If your work is on AI/ML, select the appropriate track based on the contents and applications of your work.

** Note: Scope of the conference is not limited to the above topics. Authors can select appropriate tracks to submit their papers, relevant to the specific track.

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Important Dates

Call for Papers Announcement	:	15/12/2024
Draft Paper Submission	:	From 01/01/2025 to 25/02/2025
Notification of Acceptance	:	25/03/2025
Early-Bird Registration	:	25/04/2025
Final Camera-ready Paper	:	25/05/2025
Registration	:	25/05/2025
Conference Dates (Hybrid mode)	:	From 27/06/2025 to 28/06/2025

Paper submission link: <https://cmt3.research.microsoft.com/APCI2025>

All accepted and presented papers meeting the requirements will be submitted for possible publication in the IEEE Explore® Digital Library

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Category	Indian delegates (IEEE Members)	Indian delegates (Non-IEEE Members)	Foreign delegates (IEEE members)	Foreign delegates (Non- IEEE members)
Full time Ph.D/PG research scholars	Rs 5000 + 18% GST	Rs 6000 + 18% GST	USD 250 + 18% GST	USD 300 + 18% GST
Academicians	Rs 6500 + 18% GST	Rs 7500 + 18% GST	USD 350 + 18% GST	USD 400 + 18% GST
From Industry	Rs 7500 + 18% GST	Rs 8500 + 18% GST	USD 400 + 18% GST	USD 450 + 18% GST
Accompanying person	Rs 2000 + 18% GST	Rs 2000 + 18% GST	USD 100 + 18% GST	USD 100 + 18% GST